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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven L. Scott

Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR
COMPUTER

Docket No.: 1376.700US1
Filed: August 18, 2003
Examiner: Unknown



Serial No.: 10/643,585
Due Date: N/A
Group Art Unit: 2186

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ A Communication Concerning Related Applications (2 pgs.).
- ☒ An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and a copy of 1 cited document.

If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

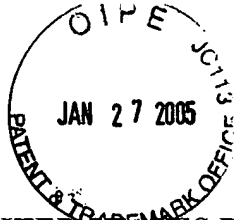
By: Thomas F. Brennan
Atty: Thomas F. Brennan
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 27th day of January, 2005.

JAMES KANYUSIK
Name

James Kanyusik
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)



S/N 10/643,585

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven L. Scott Examiner: Unknown
Serial No.: 10/643,585 Group Art Unit: 2186
Filed: August 18, 2003 Docket: 1376.700US1
Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY
MULTIPROCESSOR COMPUTER

COMMUNICATION CONCERNING RELATED APPLICATIONS

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/643,742	August 18, 2003	1376.697US1	DECOUPLED STORE ADDRESS AND DATA IN A MULTIPROCESSOR SYSTEM
10/643,586	August 18, 2003	1376.699US1	DECOUPLED VECTOR ARCHITECTURE
10/643,754	August 18, 2003	1376.724US1	RELAXED MEMORY CONSISTENCY MODEL
10/643,758	August 18, 2003	1376.729US1	REMOTE TRANSLATION MECHANISM FOR A MULTINODE SYSTEM

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 10/643,585

Filing Date: August 18, 2003

Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER

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Dkt: 1376.700US1

10/643,741

August 18,
2003

1376.733US1

MULTISTREAM PROCESSING
MEMORY-AND BARRIER-
SYNCHRONIZATION METHOD AND
APPARATUS

Respectfully submitted,

STEVEN L. SCOTT

By Applicant's Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6909

Date January 24, 2005

By Thomas F. Brennan
Thomas F. Brennan
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24th day of January, 2005.

JAMES KANYOSIK
Name

James Kanyosik
Signature

S/N 10/643,585



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Steven L. Scott	Examiner:	Unknown
Serial No.:	10/643,585	Group Art Unit:	2186
Filed:	August 18, 2003	Docket:	1376.700US1
Title:	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER		

INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No :10/643,585

Filing Date: August 18, 2003

Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER

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Dkt: 1376.700US1

The present application is a U.S. national patent application filed after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

STEVEN L. SCOTT

By his Representatives,

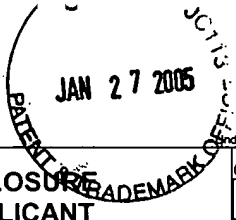
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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Minneapolis, MN 55402
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Date January 24, 2005 By Thomas F. Brennan
Thomas F. Brennan
Reg. No. 35,075

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JAMES KANYOSIK
Name

Steven Hong
Signature



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/643,585
Filing Date	August 18, 2003
First Named Inventor	Scott, Steven
Group Art Unit	2186
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 1376.700US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-2004/0044872 A1	03/04/2004	Scott, S. L.	09/04/2002
	US-2004/0162949 A1	08/19/2004	Scott, S. L., et al.	02/18/2003
	US-6,308,250	10/23/2001	Klausler, P. M.	06/23/1998
	US-RE28,577	10/21/1975	Schmidt, William G.	11/21/1973

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		GHARACHORLOO, KOUROSH, "Two Techniques to Enhance the Performance of Memory Consistency Models", <u>Proceedings of the International Conference on Parallel Processing</u> , (1991), 1-10	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached